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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: KIMURA
PATENT NO: 6,933,612 *B2*
ISSUED: August 23, 2005
FOR: Semiconductor Device with Improved Heatsink Structure
DOCKET: NEC 03USFP908

Certificate
MAR 30 2006
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR CERTIFICATE OF CORRECTION

Dear Sirs:

Kimura., the Patentee of the above-identified patent, through his attorney, hereby petition for issuance of a Certificate of Correction in the above identified patent. A Certificate of Correction (PTO form 1050) is enclosed, in duplicate. The Certificate of Correction is required to correct a significant printing errors occurring in the claims as follows:

Claim 1, Column 5, Line 57, "chin" should be -chip--.

Since the error was a Patent Office error, it is believed that the Certificate of Correction should be issued without charge to the Applicant. A correct copy of Claim 1 appears in Amendment A.

Respectfully submitted,

Norman P. Soloway

Norman P. Soloway
Attorney for Applicant
Reg. No. 24,315

HAYES SOLOWAY P.C.
3450 E. SUNRISE DRIVE,
SUITE 140
TUCSON, AZ 85718
TEL. 520.882.7623
FAX. 520.882.7643

175 CANAL STREET
MANCHESTER, NH 03101
TEL. 603.668.1400
FAX. 603.668.8567

MAR 31 2006

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 21, 2006 at Tucson Arizona.

By: _____



HAYES SOLOWAY P.C.
3450 E. SUNRISE DRIVE,
SUITE 140
TUCSON, AZ 85718
TEL. 520.882.7623
FAX. 520.882.7643

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CERTIFICATE OF CORRECTION

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DATED : August 23, 2005

INVENTOR(S) : KIMURA

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 1, Column 5, Line 57, "chin" should be --chip--.

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